

Abstract of the Disclosure:

A semiconductor component has a semiconductor substrate, an insulation layer located on the semiconductor substrate, and a semiconductor layer that is arranged on the insulation layer. A first doped terminal zone, a second doped terminal zone, and a drift zone are formed in the semiconductor layer between the first and second terminal zones. At least one of the first and second terminal zones directly adjoins the semiconductor substrate.

MPW/kf

2025-04-04 10:00:00